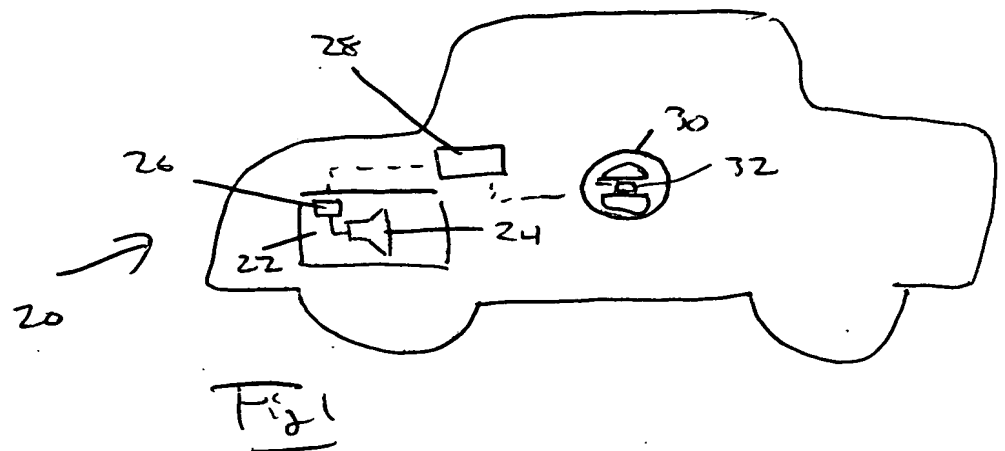
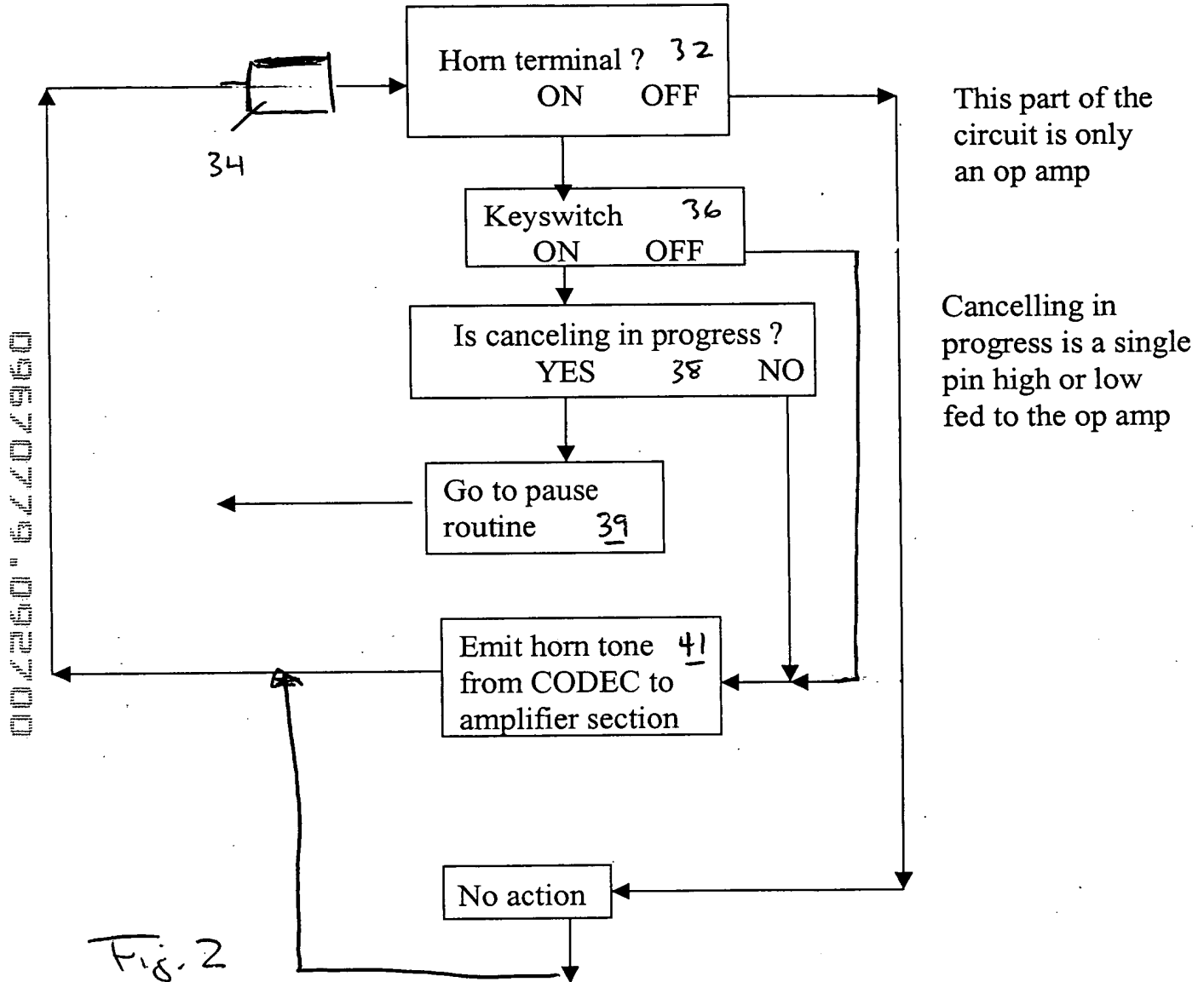
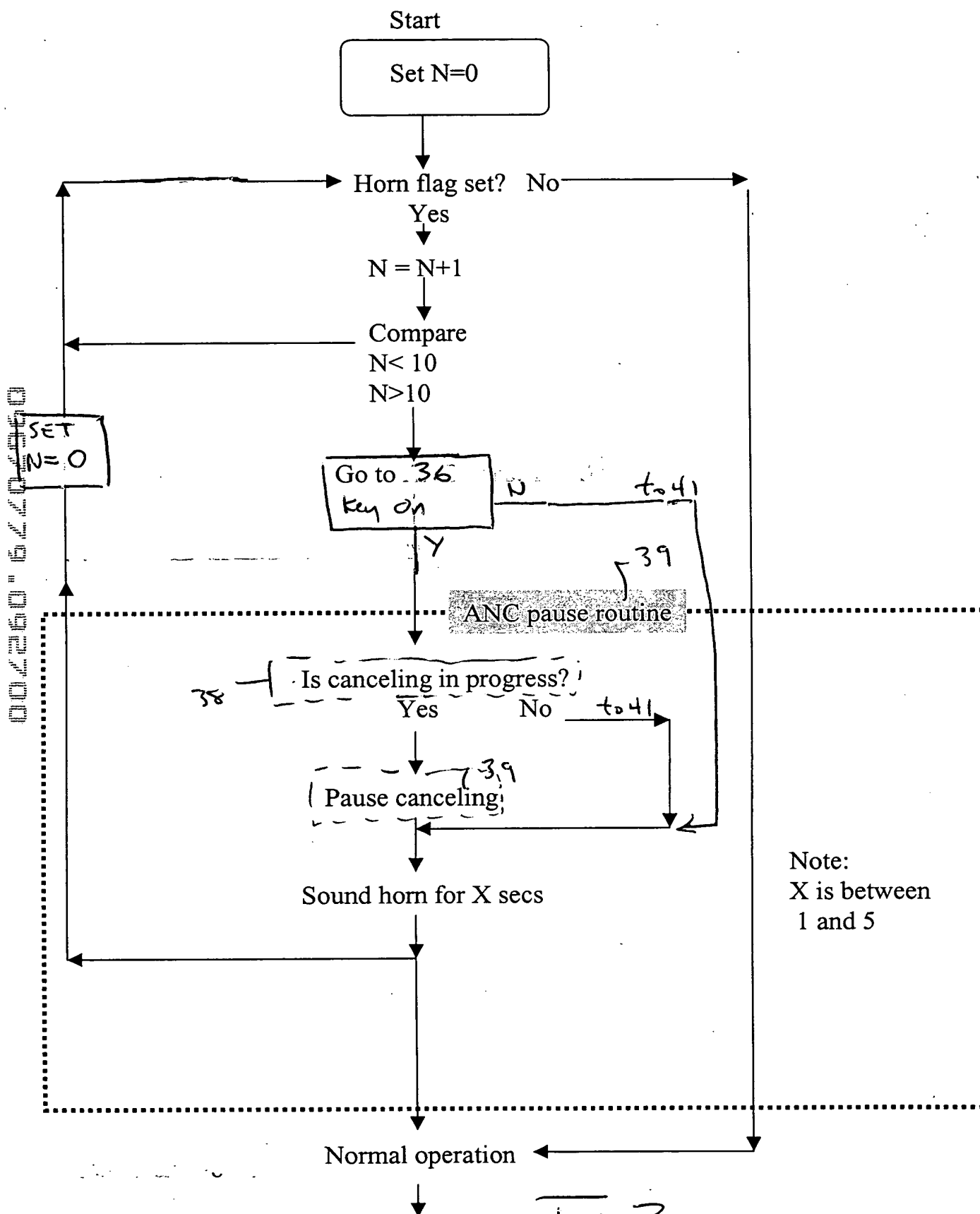


Hardware logic upstream of digital logic

Hardware filter here to eliminate noise



Fig 3